REPLACEMENT SHEET

1/12

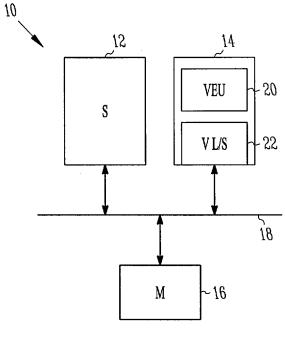
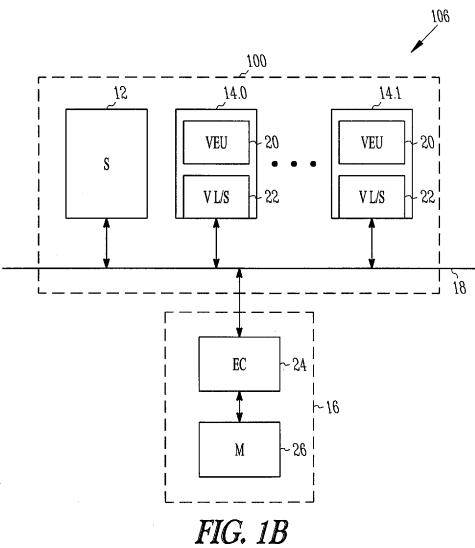
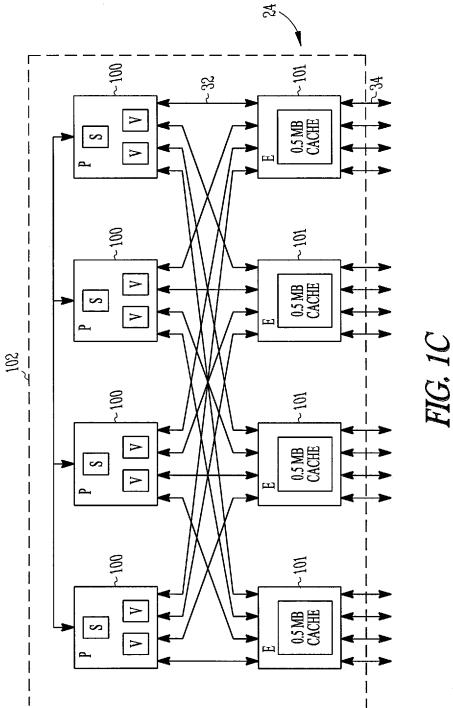


FIG. 1A



2/12



3/12

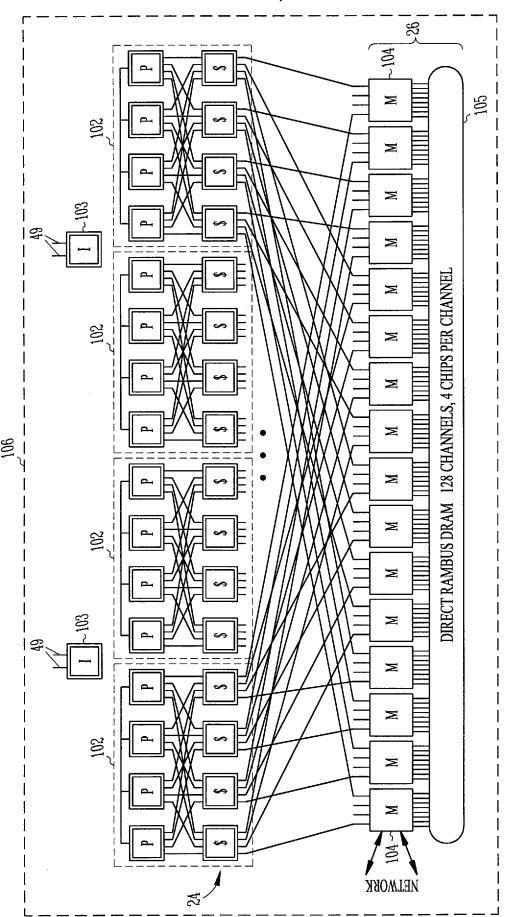


FIG. 1D

4/12

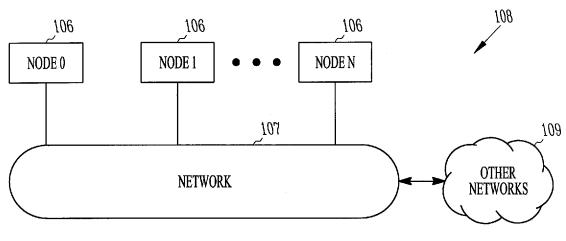


FIG. 1E

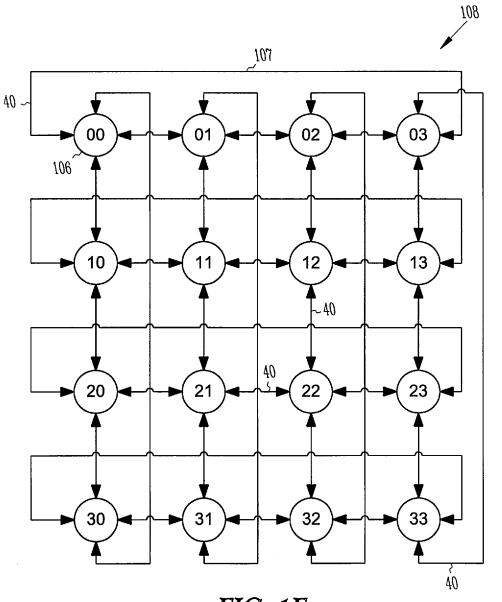


FIG. 1F

TITLE: MULTISTREAM PROCESSING MEMORY-AND BARRIER-SYNCHRONIZATION METHOD AND APPARATUS

INVENTOR'S NAME: STEVEN L. SCOTT, ET AL.
SERIAL NO.: 10/643,741 DOCKET NO.: 1376.733US1

5/12

REPLACEMENT SHEET

SCALAR SECTION

SCALAR SECTION

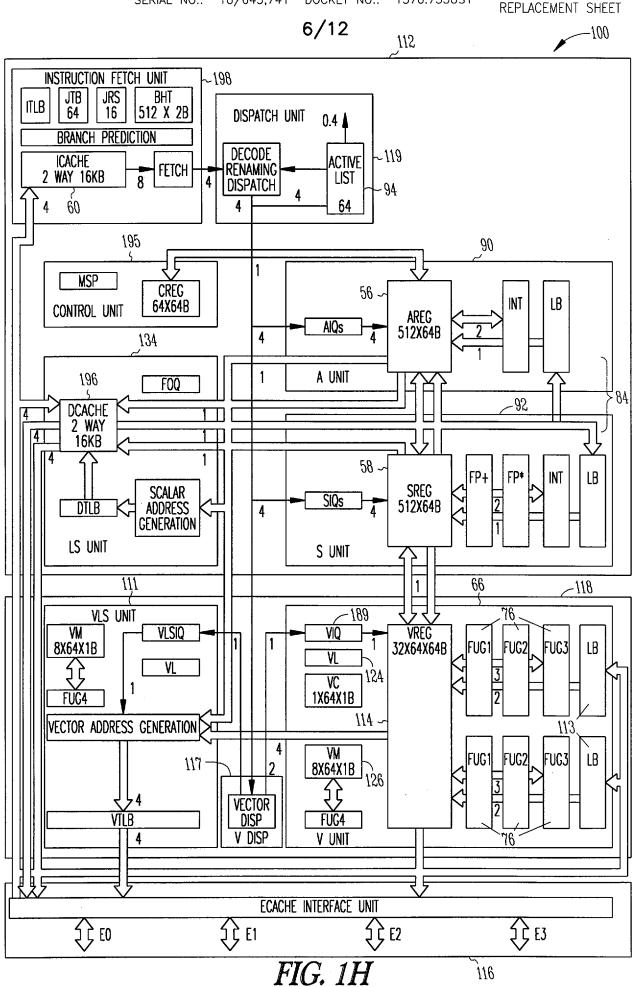
VECTOR SECTION

ECACHE INTERFACE UNIT

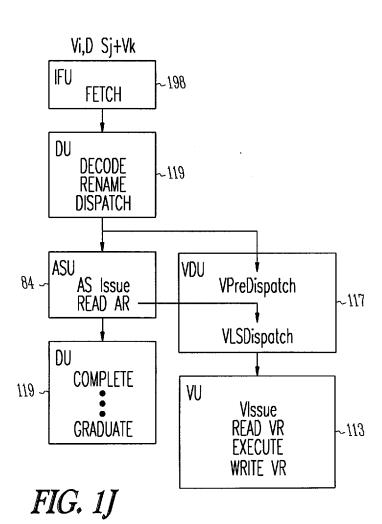
FIG. 1G

TITLE: MULTISTREAM PROCESSING MEMORY-AND BARRIER-SYNCHRONIZATION METHOD AND APPARATUS INVENTOR'S NAME: STEVEN L. SCOTT, ET AL.

SERIAL NO.: 10/643,741 DOCKET NO.: 1376.733US1



REPLACEMENT SHEET 7/12 Vi,D Vj+Vk IFU ~198 FETCH DU **DECODE** ~119 RENAME DISPATCH COMPLETE VDU **VPreDispatch** ~117 **GRADUATE** VDispatch VU VIssue READ VR ~113 **EXECUTE** WRITE VR FIG. 11



8/12

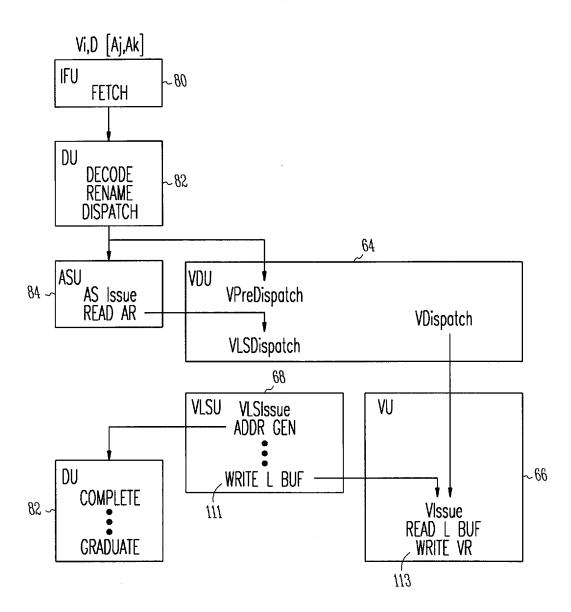
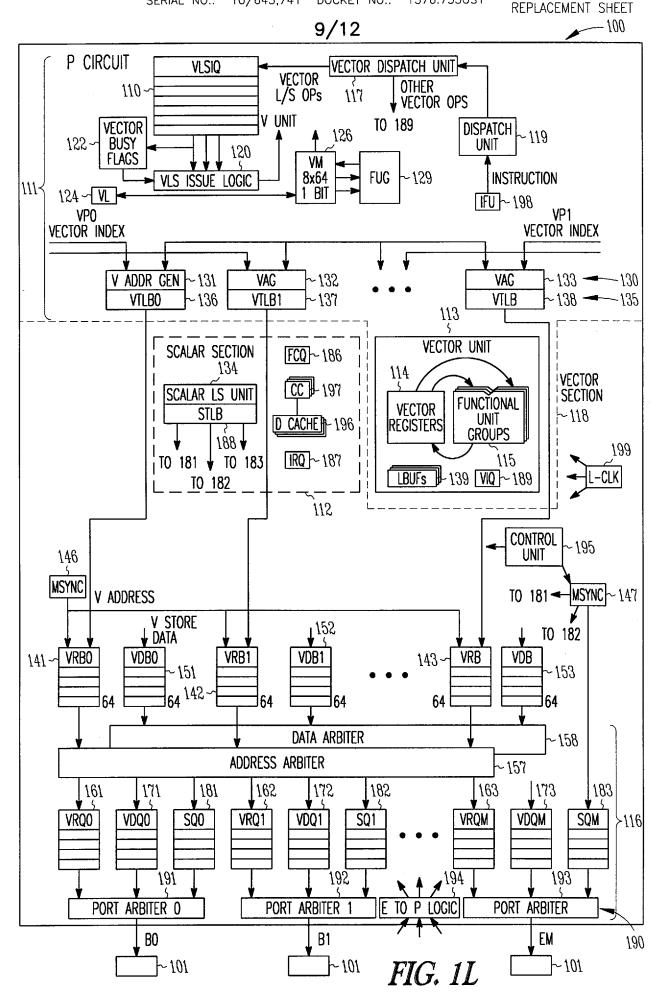
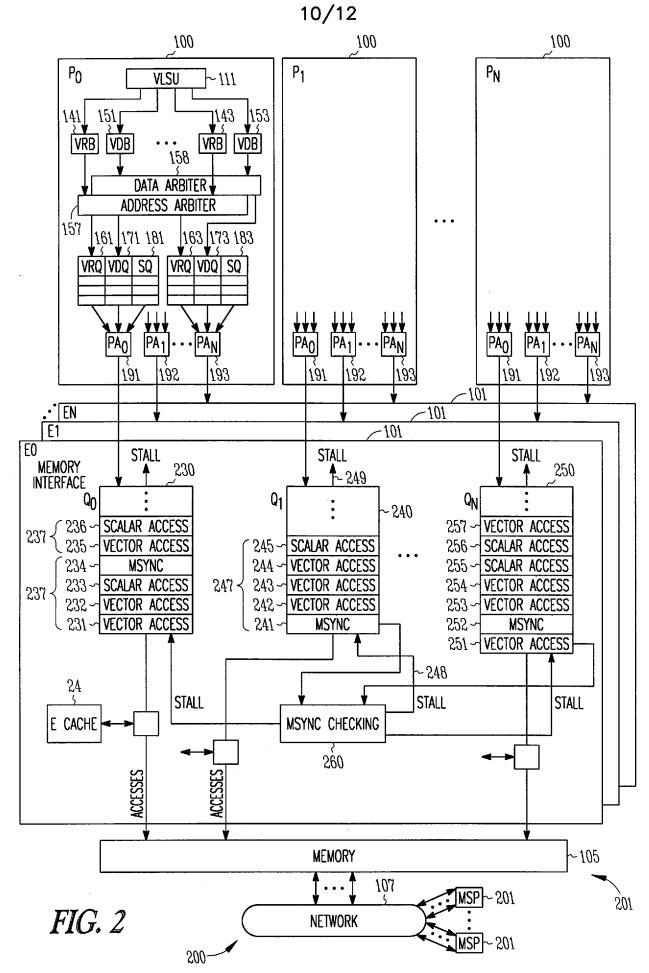


FIG. 1K

INVENTOR'S NAME: STEVEN L. SCOTT, ET AL.
SERIAL NO.: 10/643,741 DOCKET NO.: 1376.733US1





11/12

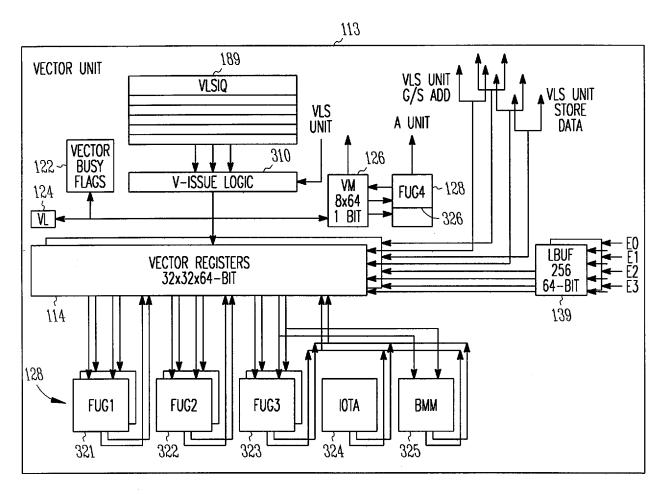


FIG. 3

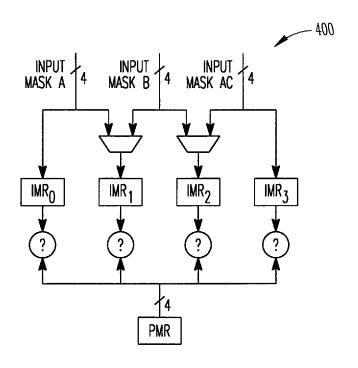


FIG. 4

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12/12

